

**LSR50M**

● **FEATURES**

- \* Halogen-free type
- \* Glass passivation chip
- \* Compliance to RoHS product
- \* Lead less chip form, no lead damage
- \* Low power loss, High efficiency
- \* High current capability
- \* Plastic package has Underwriters Laboratory Flammability Classification 94V-0

● **APPLICATION**

- \* AC/DC Power Supply
- \* Communication Equipment

● **MECHANICAL DATA**

**Case :** Packed with FRP substrate and epoxy underfilled

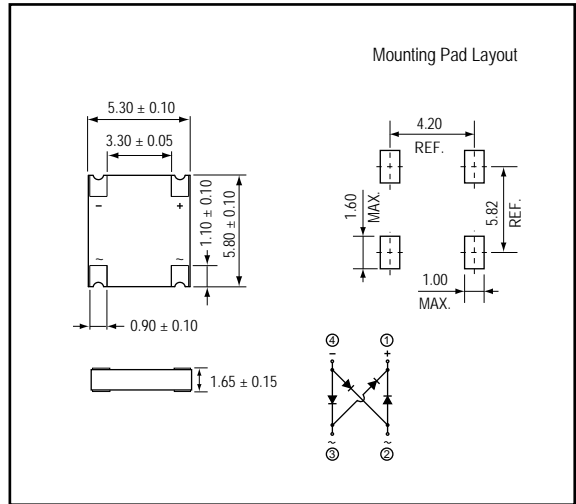
**Terminals :** Pure Tin plated (Lead-Free), solderable per MIL-STD-750, Method 2026.

**Polarity :** Laser marking symbols

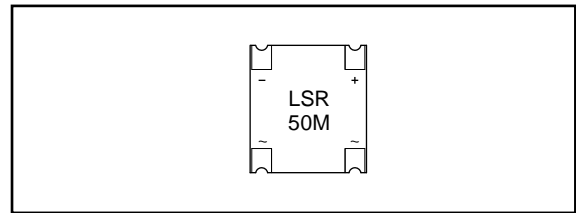
● **OUTLINE DIMENSIONS**

**Case : Z4PAK-S**

Unit : mm



● **MARKING**



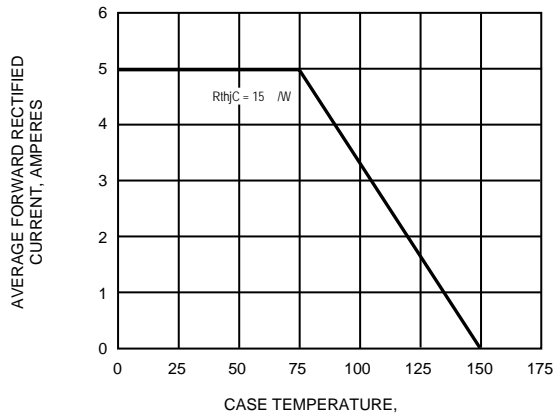
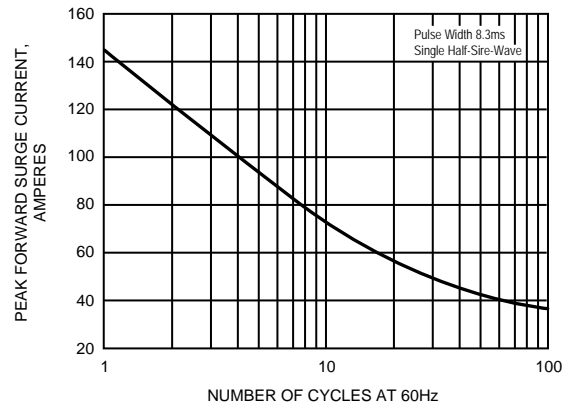
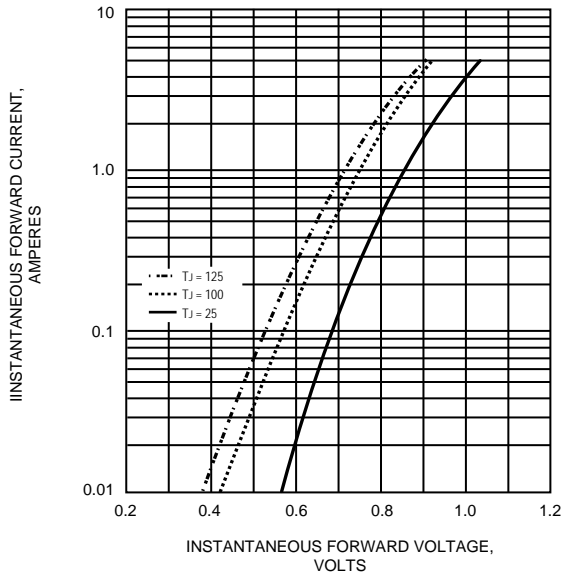
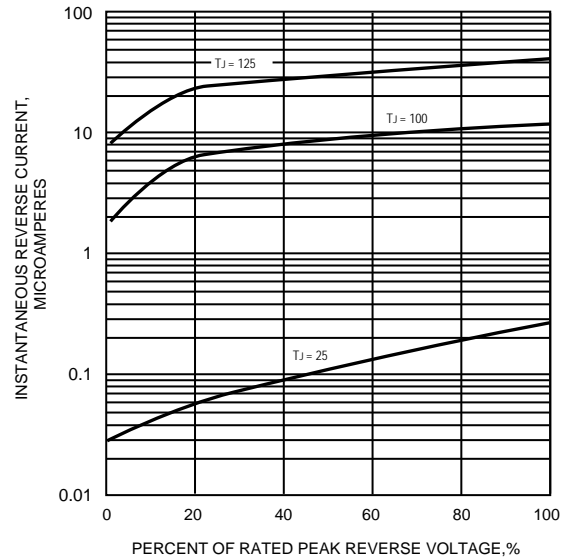
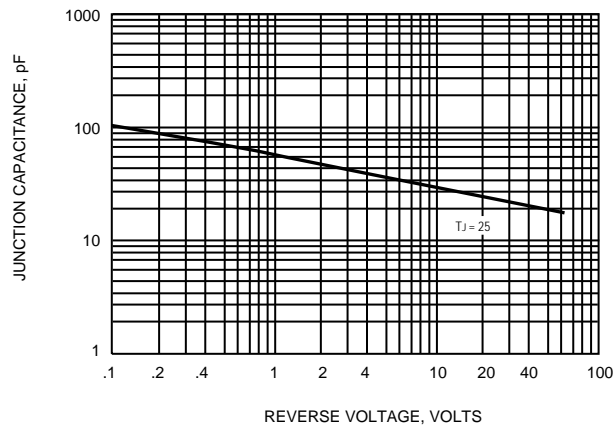
**Absolute Maximum Ratings (Ta = 25 )**

ITEM	Symbol	Conditions	Rating	Unit
Repetitive peak reverse voltage	VRRM		1000	V
Average forward current	IF(AV)		5	A
Peak forward surge current	IFSM	8.3ms single half sine-wave	145	A
Current squared time	I <sup>2</sup> t	t < 8.3ms	87	A <sup>2</sup> s
Reverse recovery time	Trr	IF = 0.5A, IR = 1.0A, Irr = 0.25A	500	nS
Operating junction and storage temperature Range	Tj, TSTG		-55 to +150	

**Electrical characteristics (Ta = 25 )**

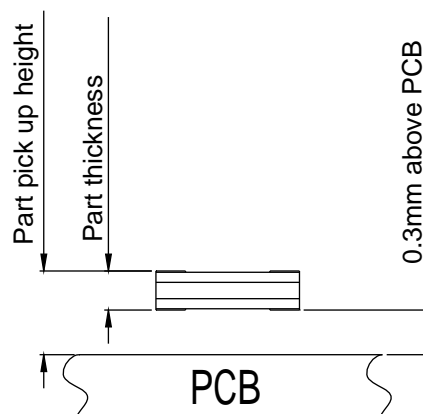
ITEM	Symbol	Conditions	Typ.	Max.	Unit
Forward voltage	VF	IF = 2A Ta = 25	0.92	-	V
		IF = 5A Ta = 25	1.05	1.30	V
		IF = 2A Ta = 125	0.78	-	V
		IF = 5A Ta = 125	0.91	-	V
Repetitive peak reverse current	IRRM	VR = Max. VRRM Ta = 25 Ta = 125	0.30 45	5 -	uA
Junction capacitance	Cj	VR = 4V, f = 1.0 MHz	44	-	pF
Thermal resistance	Rth(JA)	Junction to ambient (NOTE 1)	39	-	/W
	Rth(JC)	Junction to case (NOTE 1)	13	-	

NOTES : (1) Mounted on P.C.B. board with recommended layout copper pad areas, and heatsink.  
(2) Preliminary draft.

**FIG.1 - FORWARD CURRENT DERATING CURVE**

**FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT**

**FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS**

**FIG.4 - TYPICAL REVERSE CHARACTERISTICS**

**FIG.5 - TYPICAL JUNCTION CAPACITANCE**


## ● DESIGN AND MOUNTING FOR SURFACE MOUNT DIODES

1. In designing steps regarding PCB component layout, do not put surface mount device diodes near high voltage resistors etc, which may generate heat to diode, nor in high-density board. when designing the PCB, implement protection for the surface mount device diode from electrical damage like surge, heating source, magnetic and so on.
2. In any cases do not store diodes in the following conditions or places:
  - 2.1 When transporting diodes, keep vibration to a minimum otherwise body of diode may be broken. Diode die may then be destroyed by electrostatics.
  - 2.2 High temperature or high humidity environment.
  - 2.3 Where corrosive gas or liquid is present.
  - 2.4 Where mechanical stress or vibration exists.
  - 2.5 Where electrostatic charges are possible.
3. When using the ZOWIE Super chip diodes on assembly operation. Solder paste printing process is recommended and followed by pick and place machine. Since it was designed successfully to achieve extremely thin size, so the parameters of height and location should be adjusted on pick and place machine to avoid missing of parts during operation.
4. As ZOWIE SuperChip series are the surface mount devices with the exceptionally tiny package, whose package thickness is relatively much thinner than that of the general surface mount device, so please appropriately set the parameters for the nozzle height as well as the device thickness of the pick and place machine, which would diminish mostly the very normal stress applied upon the device by the nozzle so as to keep the yield level while implementing the mounting operation.
5. The following is a schematic drawing of recommended pick-up height of the SMT parts, the bottom of part above PCB is 0.3mm. If the parts are rejected seriously, please adjust to reduce the height appropriately.



● SMT Nozzle -

We recommend using larger nozzle (for example Z4PAK-S product can be used nozzle size 4.0mm) so that the nozzle can reach parts of the terminals at the top, in order to reduce the impact of the parts inside dice.

